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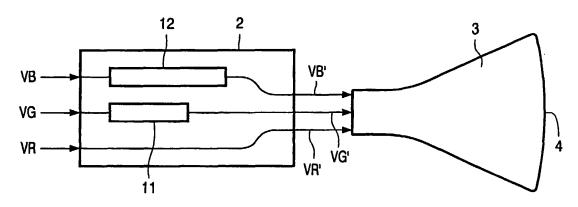
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(54) Title: IMAGE DISPLAY SYSTEM HAVING AN ANALOG DISPLAY FOR DISPLAYING MATRIX SIGNALS



(57) Abstract: Image display system comprising an analog display (3) for displaying images composed of substantially parallel lines and means for scanning the lines in a scanning direction with a scanning velocity (V). The image display system further comprises processing elements (2), which are adapted to receive a first (VR), a second (VG) and a third (VB) matrix information signal. These information signals (VR,VG,VB) are suitable to be displayed with pixel sub-component precision on a matrix display device having a plurality of pixels. The processing elements (2) are designed to shift the rendering on the display (3) of at least one of the information signals (VR,VG,VB) with respect to another one of the information signals (VR,VG,VB). As a result of this shifting, the information signals (VR,VG,VB) are rendered with sub-component precision on the analog display (3).



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B. FIELDS SEARCHED									
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C. DOCUMI	ENTS CONSIDERED TO BE RELEVANT								
Category °	Citation of document, with indication, where appropriate, of the re	elevant passages	Relevant to claim No.						
Α	US 5 604 513 A (TAKAHASHI ET AL.) 18 February 1997 (1997-02-18) column 2, line 31 - line 41 column 4, line 43 - line 65; figure 4		1						
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Furti	her documents are listed in the continuation of box C.	χ Patent family members are listed	In annex.						
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